



## 1.1 Curricular Planning and Implementation

**1.1.1. The Institution ensures effective curriculum planning and delivery through a well-planned and documented process including Academic calendar and conduct of continuous internal Assessment.**

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# 1. Academic Schedule & Calendar



**RAJIV GANDHI PROUDYOGIKI VISHWAVIDYALAYA**  
(University of Technology of Madhya Pradesh)  
**ACADEMIC CALENDAR FOR THE YEAR 2022-2023 [REVISED]**  
FOR DEGREE PROGRAMMES AND POST GRADUATE PROGRAMMES

S.No.	Particular	Ist Semester Schedule	IInd Semester Schedule
01.	Duration of Semester	July-December 2022	January-June 2023
02.	Commencement of Academic	17 <sup>th</sup> October 2022	17 <sup>th</sup> March 2023
03.	Student Induction Program (SIP) 21 Days Undergraduate	17 <sup>th</sup> October 2022	---
04.	I Sessional Exam/Mid/Sem.	26 <sup>th</sup> to 31 <sup>st</sup> December 2022	17 <sup>th</sup> April - 22 <sup>nd</sup> April 2023
05.	II Sessional Exam/Mid/Sem.	30 <sup>th</sup> Jan. - 05 <sup>th</sup> Feb. 2023	15 <sup>th</sup> May - 20 <sup>th</sup> May 2023
06.	Submission of Examination Form i. Without late fee ii. With Late Fee	30 <sup>th</sup> Jan. - 05 <sup>th</sup> Feb. 2023 06 - 09 February 2023	10 <sup>th</sup> June - 21 <sup>st</sup> June 2023 22 <sup>nd</sup> June - 27 <sup>th</sup> June 2023
07.	Last date of Teaching	03 <sup>rd</sup> February 2023	22 <sup>nd</sup> June 2023
08.	Submission of internal marks to University	Upto 28 <sup>th</sup> February 2023	29 <sup>th</sup> May - 27 <sup>th</sup> June 2023
09.	End Semester Examination (i) Theory (ii) Practical Examination	14 - 25 February 2023 26 Feb. - 03 Mar. 2023	28 <sup>th</sup> June - 12 <sup>th</sup> July 2023 13 <sup>th</sup> July - 18 <sup>th</sup> July 2023
10.	Internship/End Sem. Break	---	19 <sup>th</sup> July - 31 <sup>st</sup> July 2023
11.	Date of Result Declaration	End of March 2022	End of August 2023

## Note:

- During Mid Semester Examination, classes in the remaining periods will be conducted as per schedule.
- Depending upon monthly progress of syllabus extra classes would be organized by department during official holidays.
- III Mid Semester examination is optional. Students intending to appear in the III Mid Semester exam will contact their respective HODs for the examination.
- In case of late admission of lateral admission students, the institutions are advised to conduct extra classes to complete the syllabus within stipulated time.
- \* Winter vacation applicable for students only.



  
Controller (Exam)  
Rajiv Gandhi Proudyogiki  
Vishwavidyalaya, Bhopal



**RAJIV GANDHI PROUDYOGIKI VISHWAVIDYALAYA**  
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**ACADEMIC CALENDAR FOR THE YEAR 2022-2023 [REVISED]**  
FOR DEGREE PROGRAMMES AND POST GRADUATE PROGRAMMES

S. No.	Particular	III Semester Schedule	IV Semester Schedule
01.	Duration of Semester	July - December 2022	January - June 2023
02.	Commencement of Classes	16 <sup>th</sup> August 2022	17 <sup>th</sup> February 2023
03.	I Sessional Exam/Mid/Sem.	19 <sup>th</sup> - 24 <sup>th</sup> Sep. 2022	27 <sup>th</sup> March - 01 <sup>st</sup> April 2023
04.	II Sessional Exam/Mid/Sem.	16 <sup>th</sup> - 22 <sup>nd</sup> Nov. 2022	01 <sup>st</sup> May - 06 <sup>th</sup> May 2023
05.	Dussehra Holiday	02 <sup>nd</sup> - 09 <sup>th</sup> Oct. 2022	-----
06.	Submission of Examination Form i. Without late fee ii. With Late Fee	10 <sup>th</sup> - 26 <sup>th</sup> Dec. 2022 27 <sup>th</sup> Dec. 2022 - 03 <sup>rd</sup> Jan. 2023	10 <sup>th</sup> June - 20 <sup>th</sup> June 2023 21 <sup>st</sup> June - 25 <sup>th</sup> June 2023
07.	Diwali Vacation	23 <sup>rd</sup> - 30 <sup>th</sup> Oct. 2022	-----
08.	Last date of Teaching	31 <sup>st</sup> December 2022	27 <sup>th</sup> May 2023
09.	Submission of Mid Semester & Sessional Marks to University	04 <sup>th</sup> - 09 <sup>th</sup> Jan. 2023	29 <sup>th</sup> May - 26 <sup>th</sup> June 2023
10.	End Semester Examination (i) Theory (ii) Practical Examination	10 <sup>th</sup> - 21 <sup>st</sup> Jan. 2023 23 <sup>rd</sup> - 31 <sup>st</sup> Jan. 2023	27 <sup>th</sup> June - 11 <sup>th</sup> July 2023 12 <sup>th</sup> July - 17 <sup>th</sup> July 2023
11.	Submission of Practical marks to University	On the date of Practical Examination	On the date of Practical Examination
12.	Internship/Preparation Leave	---	29 <sup>th</sup> May - 26 <sup>th</sup> June 2023
13.	Declaration of result	In the Month of February 2023	In the Month of August 2023

**Note:**

- During Mid Semester Examination, classes in the remaining periods will be conducted as per schedule.
- Depending upon monthly progress of syllabus extra classes would be organized by department during official holidays.
- III Mid Semester examination is optional. Students intending to appear in the III Mid Semester exam will contact their respective HODs for the examination.
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- \* Winter vacation applicable for students only.



  
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Vishwavidyalaya, Bhopal



**RAJIV GANDHI PROUDYOGIKI VISHWA VIDYALAYA**  
(University of Technology of Madhya Pradesh)  
**ACADEMIC CALENDAR FOR THE YEAR 2022-2023 [REVISED]**  
FOR DEGREE PROGRAMMES AND POST GRADUATE PROGRAMMES

S. No.	Particular	V Semester Schedule	VI Semester Schedule
01.	Duration of Semester	July - December 2022	January - June 2023
02.	Commencement of Classes	01 <sup>st</sup> August 2022	02 <sup>nd</sup> January 2023
03.	I Sessional Exam/Mid/Sem.	13 <sup>th</sup> - 17 <sup>th</sup> Sep. 2022	13 <sup>th</sup> - 17 <sup>th</sup> February 2023
04.	II Sessional Exam/Mid/Sem.	01 <sup>st</sup> - 05 <sup>th</sup> Nov. 2022	17 <sup>th</sup> to 25 <sup>th</sup> March 2023
05.	Dussehra Holiday	02 <sup>nd</sup> - 09 <sup>th</sup> Oct. 2022	----
06.	Submission of Examination Form		
	i. Without late fee ii. With Late Fee	09 <sup>th</sup> - 28 <sup>th</sup> Nov. 2022 29 <sup>th</sup> Nov.- 06 <sup>th</sup> Dec. 2022	01 <sup>st</sup> - 19 <sup>th</sup> April 2023 20 <sup>th</sup> - 26 <sup>th</sup> April 2023
07.	Diwali Vacation	23 <sup>rd</sup> - 30 <sup>th</sup> Oct. 2022	----
08.	Last date of Teaching	30 <sup>th</sup> November 2022	20 <sup>th</sup> April 2023
09.	Submission of Mid Semester & Sessional Marks to University	01 <sup>st</sup> - 06 <sup>th</sup> Dec. 2022	20 <sup>th</sup> - 26 <sup>th</sup> April 2023
10.	End Semester Examination		
	(i) Theory (ii) Practical Examination	07 <sup>th</sup> - 17 <sup>th</sup> Dec. 2022 19 <sup>th</sup> - 24 <sup>th</sup> Dec. 2022	27 <sup>th</sup> April - 20 <sup>th</sup> May 2023 21 <sup>st</sup> - 31 <sup>st</sup> May 2023
11.	Submission of Practical marks to University	On the date of Practical Examination	On the date of Practical Examination
12.	End Semester Break / Internship	---	01 <sup>st</sup> - 30 <sup>th</sup> June 2023
13.	Winter* Vacation for teachers	25 <sup>th</sup> - 31 <sup>st</sup> December 2022	01 <sup>st</sup> - 30 <sup>th</sup> June 2023
14.	Declaration of Result	In the Month of January 2023	In the Month of June 2023

**Note:**

- During Mid Semester Examination, classes in the remaining periods will be conducted as per schedule.
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- III Mid Semester examination is optional. Students intending to appear in the III Mid Semester exam will contact their respective HODs for the examination.
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Rajiv Gandhi Proudyogiki  
Vishwavidyalaya, Bhopal



# RAJIV GANDHI PROUDYOGIKI VISHWAVIDYALAYA

(University of Technology of Madhya Pradesh)

## ACADEMIC CALENDAR FOR THE YEAR 2022-2023 [REVISED]

FOR DEGREE PROGRAMMES AND POST GRADUATE PROGRAMMES

S.No.	Particular	VII & IX Semester Schedule	VIII & X Semester Schedule
01.	Duration of Semester	July - December 2022	January-June 2023
02.	Commencement of Classes	04 <sup>th</sup> July 2022	02 <sup>nd</sup> January 2023
03.	I Sessional Exam/Mid/Sem.	22 <sup>nd</sup> to 27 <sup>th</sup> August 2022	13 <sup>th</sup> - 17 <sup>th</sup> February 2023
04.	II Sessional Exam/Mid/Sem.	17 <sup>th</sup> to 22 <sup>nd</sup> October 2022	17 <sup>th</sup> to 25 <sup>th</sup> March 2023
05.	Dussehra Holiday	2 <sup>nd</sup> to 09 <sup>th</sup> October 2022	----
06.	Submission of Examination Form i. Without late fee ii. With Late Fee	25 <sup>th</sup> Oct. - 13 <sup>th</sup> Nov. 2022 14 <sup>th</sup> - 21 <sup>st</sup> November 2022	01 <sup>st</sup> - 19 <sup>th</sup> April 2023 20 <sup>th</sup> - 26 <sup>th</sup> April 2023
07.	Diwali Vacation	23 <sup>rd</sup> - 30 <sup>th</sup> October 2022	----
08.	Last date of Teaching	16 <sup>th</sup> November 2022	20 <sup>th</sup> April 2023
09.	Submission of Mid Semester & Sessional Marks to University	14 <sup>th</sup> to 21 <sup>st</sup> November 2022	20 <sup>th</sup> - 26 <sup>th</sup> April 2023
10.	End Semester Examination (i) Theory (ii) Practical Examination	22 <sup>nd</sup> Nov. - 14 <sup>th</sup> Dec. 2022 15 <sup>th</sup> to 24 <sup>th</sup> December 2022	27 <sup>th</sup> April - 20 <sup>th</sup> May 2023 21 <sup>st</sup> - 31 <sup>st</sup> May 2023
11.	Submission of Practical marks to University	On the date of Practical Examination	On the date of Practical Examination
12.	End Semester Break / Internship	----	01 <sup>st</sup> - 30 <sup>th</sup> June 2023
13.	Winter* /Summer Vacation for teachers	25 <sup>th</sup> - 31 <sup>st</sup> December 2022	01 <sup>st</sup> - 30 <sup>th</sup> June 2023
14.	Declaration of result of final Year	In the Month of January 2023	In the Month of June 2023

### Note:

- During Mid Semester Examination, classes in the remaining periods will be conducted as per schedule.
- Depending upon monthly progress of syllabus extra classes would be organized by department during official holidays.
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- \* Winter vacation applicable for students only.



  
Controller (Exam)  
Rajiv Gandhi Proudyogiki  
Vishwavidyalaya, Bhopal

## 2. Academic Schedule & Calender (MCBU)

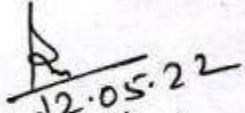
मध्यप्रदेश शासन  
उच्च शिक्षा विभाग  
वल्लभ, भवन मंत्रालय-462004

:: आदेश ::

भोपाल, दिनांक 12/05/2022

क्रमांक: 330/ub/सीसी/2022/अड़तीस :: राज्य शासन एतद् द्वारा सत्र 2022-23 के लिये मध्यप्रदेश के शासकीय/अनुदान प्राप्त अशासकीय/निजी अशासकीय महाविद्यालयों में स्नातक तथा स्नातकोत्तर कक्षाओं हेतु प्रवेश नियम, मार्गदर्शी सिद्धांत, अकादमिक कैलेंडर एवं ऑनलाइन प्रवेश समय-सारणी जारी किये जाते हैं।

संलग्न- प्रवेश नियम/मार्गदर्शी सिद्धांत।

  
12.05.22  
(संघमित्रा गौतम)

उप सचिव

म.प्र.शासन, उच्च शिक्षा विभाग

पू0क्रमांक: 331/ub/सीसी/2022/  
प्रतिलिपि :

भोपाल, दिनांक 12/05/2022

1. प्रमुख सचिव, माननीय राज्यपाल, राजभवन सचिवालय, मध्यप्रदेश भोपाल, म.प्र.।
2. निज सहायक, माननीय मंत्री जी, उच्च शिक्षा, म.प्र.।
3. स्टाफ आफीसर, अपर मुख्य सचिव, म.प्र. शासन, उच्च शिक्षा विभाग, मंत्रालय।
4. आयुक्त, उच्च शिक्षा, म.प्र. सतपुड़ा भवन, भोपाल।
5. अध्यक्ष, निजी विश्वविद्यालय विनियामक आयोग, भोपाल, म.प्र.।
6. कुलसचिव, समस्त परम्परागत विश्वविद्यालय भोपाल/इन्दौर/ग्वालियर/जबलपुर/रीवा/उज्जैन/छतरपुर एवं छिन्दवाड़ा म.प्र.।
7. कुलसचिव, समस्त निजी विश्वविद्यालय, म.प्र.।
8. समस्त क्षेत्रीय अतिरिक्त संचालक, उच्च शिक्षा, म.प्र.।
9. सी.ई.ओ. एम.पी.ऑनलाइन, द्वितीय तल डी.बी.मॉल, एम.पी.नगर भोपाल म.प्र. की ओर कार्यवाही हेतु।
10. प्राचार्य, शासकीय/अशासकीय महाविद्यालय, म.प्र.।
11. प्रभारी आई.टी. शाखा, उच्च शिक्षा, म.प्र., सतपुड़ा भवन, भोपाल की ओर वेबसाइट पर अपलोड करने हेतु।

..... की ओर सूचनार्थ एवं कार्यवाही हेतु।



उप सचिव

म.प्र. शासन, उच्च शिक्षा विभाग

अकादमिक कैलेंडर सत्र 2022-23 (सेमेस्टर के लिए)

अकादमिक कार्य	स्नातकोत्तर प्रथम/तृतीय	स्नातकोत्तर द्वितीय/चतुर्थ
कक्षाएँ प्रारंभ सिर्फ स्नातकोत्तर प्रथम / तृतीय सेमेस्टर	01 जुलाई 2022	21 दिसम्बर 2022
शैक्षणिक कार्य	01 जुलाई 2022 से 30 अक्टूबर 2022	21 दिसम्बर 2022 से 13 अप्रैल 2023
सी.सी.ई. (सतत समग्र मूल्यांकन)	सितम्बर तृतीय सप्ताह	मार्च द्वितीय सप्ताह
प्रायोगिक परीक्षाएँ	18 अक्टूबर 2022 से 30 अक्टूबर 2022 के मध्य	01 अप्रैल से 13 अप्रैल 2023 के मध्य
परीक्षा पूर्व तैयारी अवकाश	08 नवम्बर 2022 से 15 नवम्बर 2022	13 अप्रैल 2023 से 19 अप्रैल 2023
सेमेस्टर एवं एटीकेटी परीक्षा	16 नवम्बर 2022 से 11 दिसम्बर 2022	20 अप्रैल 2023 से 16 मई 2023
सेमेस्टर अंतराल (ब्रेक) विद्यार्थियों के लिए	12 दिसम्बर 2022 से 20 दिसम्बर 2022	17 मई से 30 जून 2022
परीक्षा परिणामों की घोषणा	31 दिसम्बर 2022 तक	15 जून 2023 तक

- प्रवेश उत्सव कार्यक्रम : अगस्त (प्रथम सप्ताह) 2022
- छात्रसंघ गठन : अगस्त/सितम्बर 2022
- खेलकूद/एन.सी.सी./एन.एस.एस./युवा उत्सव/  
दीक्षान्त समारोह एवं अन्य गतिविधियाँ : माह अक्टूबर 2022 तक पूर्ण कर  
ली जाएँ
- दीपावली अवकाश : दिनांक 24 अक्टूबर 2022 से 29  
अक्टूबर 2022 तक (05 कार्य दिवस)
- स्नेह सम्मेलन/वार्षिकोत्सव/पुरस्कार वितरण  
वार्षिक पत्रिका का प्रकाशन एवं विमोचन : फरवरी 2023 द्वितीय सप्ताह

टीप :-

- (1) अपरिहार्य कारणवश शैक्षणिक कार्य निर्धारित गानक दिवसों से कम होने की दशा में, महाविद्यालय/विधि स्तर पर क्षणिक कालखण्डों की अवधि में आवश्यकतानुसार वृद्धि कर शैक्षणिक दिवसों की पूर्ति की जावेगी ताकि अकादमिक कैलेंडर का पालन समथानुसार सुनिश्चित हो सके।
- (2) स्नातकोत्तर तृतीय सेमेस्टर में प्रवेश हेतु नवीनीकरण प्रक्रिया को अपनाते हुए शैक्षणिक कार्य प्रारंभ करना सुनिश्चित किया जायेगा।
- (3) शैक्षणिक कार्य दिवस कम होने पर अतिरिक्त कक्षाएँ संचालित कर पाठ्यक्रम पूर्ण किया जावेगा।

स्नातकोत्तर प्रथम/तृतीय सेमेस्टर के कार्य दिवस की गणना  
सत्र 2022-23

क्र.	माह	दिवस	अवकाश	कार्य दिवस
1	जुलाई 2022	31	5 रविवार + 0 अवकाश	26
2	अगस्त 2022	31	4 रविवार + 4 अवकाश	23
3	सितम्बर 2022	30	4 रविवार + 0 अवकाश	26
4	अक्टूबर 2022	31	5 रविवार + 3 अवकाश	23
5	नवम्बर 2022	30	4 रविवार + 2 अवकाश	24
6	11 दिसम्बर 2022 तक	11	2 रविवार + 0 अवकाश	9
	कुल दिवस	164	33	131

स्नातकोत्तर प्रथम/तृतीय सेमेस्टर के शैक्षणिक कार्य दिवस की गणना  
सत्र 2022-23

क्रमांक	विवरण	कार्य दिवस
1.	01 जुलाई 2022 से 11 दिसम्बर 2022 तक कुल कार्य दिवस	131
2.	अवकाश एवं शैक्षणिक गतिविधि/परीक्षा हेतु अशैक्षणिक दिवस 1. स्थानीय अवकाश- 03 2. दीपावली अवकाश- कुल 5 कार्यदिवस 3. महाविद्यालय स्तर गतिविधियां- 08 कार्य दिवस 4. परीक्षा- 25 कार्य दिवस	41
3.	कुल शैक्षणिक दिवस (1-2) (131-41)	90

स्नातकोत्तर द्वितीय/चतुर्थ सेमेस्टर के कार्य दिवस की गणना  
सत्र 2022-23

क्रमांक	माह	दिवस	अवकाश	कार्य दिवस
1.	21 दिसम्बर 2022 से	11	1 रविवार + 0 अवकाश	10
2.	जनवरी 2023	31	5 रविवार + 1 अवकाश	25
3.	फरवरी 2023	28	4 रविवार + 1 अवकाश	23
4.	मार्च 2023	31	4 रविवार + 3 अवकाश	24
5.	अप्रैल 2023	30	5 रविवार + 5 अवकाश	20
6.	मई 2023	31	4 रविवार + 1 अवकाश	26
7.	जून 2023	30	4 रविवार + 1 अवकाश	25
	कुल दिवस	192	192-39	153

स्नातकोत्तर द्वितीय/चतुर्थ सेमेस्टर के शैक्षणिक कार्य दिवस की गणना  
सत्र 2022-23

क्रमांक	विवरण	कार्य दिवस
1.	21 दिसम्बर 2022 से 16 मई 2023 तक कुल कार्य दिवस	153
2.	अवकाश एवं शैक्षणिक गतिविधि/परीक्षा हेतु अशैक्षणिक दिवस 1. महाविद्यालय स्तर गतिविधियां- 03 कार्य दिवस 2. परीक्षा- 22 कार्य दिवस 3. ग्रीष्म अवकाश 37 कार्य दिवस	62
3.	कुल शैक्षणिक दिवस (1-2) (153-62)	91



कार्य दिवस की गणना सत्र 2022-23  
(वार्षिक पद्धति - स्नातक प्रथम/द्वितीय/तृतीय वर्ष के लिए)


(अ)	अवकाश एवं शैक्षणोत्तर गतिविधियों का विवरण	
1.	रविवार	52
2.	सामान्य अवकाश	18 कार्य दिवस
3.	स्थानीय अवकाश	03 कार्य दिवस
4.	दीपावली अवकाश	05 कार्य दिवस
5.	छात्रराघ गठन/महाविद्यालयीन सांस्कृतिक गतिविधियां आदि	15 कार्य दिवस
	योग	93
(ब)	परीक्षा/ग्रीष्मावकाश के अशैक्षणिक दिवस	
1	परीक्षा पूर्व तैयारी	06 कार्य दिवस
2.	परीक्षा अवधि	35 कार्य दिवस
3.	ग्रीष्म अवकाश	36 कार्य दिवस
	योग	77 कार्य दिवस
(स)	कुल अशैक्षणिक दिवस (अ+ब) (93+77) = 170	170 कार्य दिवस
(द)	कुल शैक्षणिक दिवस (365-170) = 195	195 शैक्षणिक कार्य दिवस

नोट- अपरिहार्य कारणवश शैक्षणिक कार्य निर्धारित मानक 180 दिवस से कम होने की दशा में, महाविद्यालय/वि.वि स्तर पर शैक्षणिक कालखण्ड की अवधि में आवश्यकतानुसार वृद्धि कर शैक्षणिक दिवस की पूर्ति की जावेगी ताकि अकादमिक कैलेंडर का पालन समयानुसार सुनिश्चित हो सके। शैक्षणिक कार्य दिवस कम होने पर अतिरिक्त कक्षाएं संचालित कर पाठ्यक्रम पूर्ण किया जावे।

उप सचिव  
मध्यप्रदेश शासन  
उच्च शिक्षा विभाग



### 3. Subject Preferences Circular (EC Dept.)



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#### Circular

**Department: Electronics and Communication**

Record No: BTIRT/EC/CN/06/22/0227  
Semester - odd

For Academic Year: 2022-23  
Date: 06/06/2022

All the faculty members are requested to fill the subject preferences form (Theory and Laboratory courses) as per RGPV University syllabus for the odd semester of the academic year 2022-23.

  
Head of Department

Copy To:

1. Circulate among faculty members.
2. HOD File.
3. Time Table Incharge.



## 4. Subject Preferences Form (Sample)



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### Subject Preferences Form

Date: 06.06.2022

#### Department: Electronics and Communication

**Note:** All faculty members are requested to give their subject choices referring to the RGPV curriculum. You are suggested to choose at least 3 theoretical and 2 practical subjects.

Name of Faculty: Mr. Vikash Panthi

Subject Code	Subject Name	Contact Hours/week (Theory)
BT301	Mathematics-III	3
EC302	EMI	3
EC303	DSD	3
EC304	ED	3
EC305	NA	3
EC 501	MP	2
EC 502	DC	2
EC 503	CNTL	4
EC 504	EMT	3
EC701	VLSI Design	2
EC702	Microwave Engg.	3
EC703	Internet of Things	3
MEDC-101	Advanced Mathematics	3
MEDC-102	MCSD	3
MEDC-103	DSP	3
MEDC-104	VLSI Design	3
MEDC-105	DC and CN	3
MEDC-301	ITC	3
MEDC-302	ADC	3

Subject Code	Subject Name	Contact Hours/week (Practical)
EC303	Digital System Design	2
EC304	Electronic Devices	2
EC305	Network Analysis	2
EC306	EMI Lab	4
EC307	Internship-I	4
EC 501	Microprocessor	2
EC 502	DC	2
EC 505	CNTL Lab	4
EC 506	Matlab Programming	4
BT-407	Internship-II	6
EC 508	Minor Project 1	4
EC701	VLSI Design	2
EC704	Microwave Lab	6
EC705	I.O.T. Lab	6
EC706	Major Project-I	8
EC707	Internship -III	6
MEDC-106	Lab-I (102,103)	6
MEDC-107	Lab-II (104,105)	6
MEDC-303	Seminar	4
MEDC-304	Dissertation Part I	8

**Note:** Each faculty must give choice of minimum 3 subject and 2 Lab work (As per above mention).

Subject Code	Theory Preferences				Lab work Preferences	
	1	2	3	4	1	2
	EC-303	EC-501	EC-701	MEDC-104	EC-303	EC-501

Panthi  
Faculty Sign



Megh  
Head of Department

## 5. Subject Allotment (Sample)



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Department of Electronics and Communication Engineering



Name of Faculty	Subject Allotment										Total Load	
	Subject 1		Subject 2		Subject 3		Subject 3		Subject 3			
	L	T/P	L	T/P	L	T/P	L	T/P	L	T/P		
1 Megha Soni	MEDC-102 Micro controller system Design	3	1	MEDC-106 Lab-I (102,103)	0	0	EC705 - Major Project-I	0	0	0	2	12
2 Priyanka Patariya	MEDC-103 DSP Application	3	1	MEDC-302(A) Advanced Digital Communication	3	1		0	0	0	0	8
3 Jyoti Dangi	MEDC-104 VLSI Design	3	1	MEDC-107 Lab-II (104,105)	0	0	MEDC-304 Dissertation I	0	0	0	2	12
4 Dr. Biresh Gupta	BT301 Mathematics-III	3	1	MEDC-101 Advanced Mathematics	3	1	MEDC-304 Dissertation I	0	0	0	2	10
5 Naveen Khare	EC302 EMI	3	1	EC III SEM Internship-I	0	0		0	0	0	0	12
6 Vikash Panthi	EC303 Digital System Design	3	0	MEDC-105 Data Communication and Computer Networks	3	1	MEDC-304 Dissertation I	0	0	0	2	11
7 Ankur Namdeo	EC304 Electronic Devices	3	0	EC705 - Major Project-I	0	0	EC705 - Major Project-I	0	0	0	4	11
8 Kalpana Bhargava	EC 504 (A) EMT	3	0	EC 703(B) IOT	3	0		0	0	0	0	12
9 New Faculty	EC501 Microprocessor & its Application	2	1	MEDC-301(A) Information Theory & Coding	3	1	EC III SEM Internship-I	0	0	0	2	11
10 Samarth Jain	EC 702(A) Microwave Engg	3	1		0	0		0	0	0	0	10
11 Anshu Gupta	EC 503 (A) CNTI	4	0		0	0		0	0	0	0	8
12 Diksha Jain	EC305 Network Analysis	3	0	EC III SEM Internship-I	0	0		0	0	0	0	7
13 Sneha Thakur	EC 506 Matlab Programming	0	0	MEDC-304 Dissertation I	0	0		0	0	0	0	6
14 Jitendra Kumar Ahirwar	BT-407 Internship-II	0	0		0	0		0	0	0	0	6
15 Anshul Jain	EC 508 Minor Project I	0	0	MEDC-303 Seminar	0	0		0	0	0	0	6
16 Kajal Bairagi	EC502 Digital Communication	2	1	MEDC-303 Seminar	0	0		0	0	0	0	7
17 Zaan Khan	Internship-III	0	0		0	0		0	0	0	0	6
18 Anjali Jain	EC701 VLSI Design	2	1	EC705 - Major Project-I	0	0		0	0	0	0	7

Dr. Veeresh Fuskele  
Principal BTIRT, Sagar



Mrs. Megha Soni  
HoD EC Department

## 6. Time Table Committee Meeting



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### CIRCULAR


Ref: BTIRT/TT/22/4023

Date: 17.06.2022

It is hereby to inform that the Time Table Committee Meeting of the Odd Semester of Academic year 2022-2023 is to be conducted on 20.06.2022 at Principal Office by 11.00 am. All the members of the time table committee are requested to attend the meeting for upcoming timetable preparation & finalization process.

#### Agenda for the Meeting.

1. Review on previous semester minutes of meeting.
2. Discussion on laboratories slots for shared laboratories on 2023-2024 academic year.
3. Finalization of department electives and allied Courses.
4. Finalization of Time Table for all the departments.

  
Mr. Hemant Jain  
Time Table In charge



  
Dr. Veeresh Fuskele  
Principal BTIRT

#### Copy to:

1. File Chairperson/Principal
2. All HODs(Dept of EC/ME/CE/CSE/AI/EX/MBA)
3. Office Copy



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Ref. No. BTIRT/EC/CN/SE/22/0229

Date: 21/06/2022

**Minutes of Meeting**

A Time Table Committee Meeting of the Odd Semester of Academic year 2022-2023 was convened on 21/06/2022 at 11.00 AM in Principal Office. All the members of the time table committee were requested to attend the meeting. The agenda of the meeting was to discuss for upcoming timetable preparation & finalization process. The Meeting was chaired by Dr. Veeresh Fuskele, Principal BTIRT.

Following time table committee Members were present for the meeting:

S.No	Name	Designation
1	Dr. Sujit Singh Thakur	Incharge Diploma
2	Mr. Ravindra Raj	HOD/CE
3	Mr. Pranjal Khare	HOD/CS
4	Mrs. Megha Soni	HOD/ECE,EX
5	Mr. Gaurav Khare	HOD/ME
6	Mr. Hemant Jain	HOD/First Year
7	Mr. Hemant Jain	Time Table In charge





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Following points were discussed in the meeting as per the agenda

1. Principal welcomed the members of the Time Table committee.
2. Principal suggested approving the minutes of the previous minutes of meeting.
3. The committee verified the format of the time table submitted by the departments and proposed necessary corrections.
4. Committee verified the submitted slots of laboratories by faculty concern of various departments and the Principal suggested avoiding overlapping of the laboratories from the various Departments.
5. Principal recommended that one period to be allocated for library/Internet and one period to be allocated for placement training for all classes and nominate faculties for the same.
6. Principal suggested the committee members to ensure the classes to be followed as per the schedule.

  
Dr. Veeresh Fuskele  
Principal BTIRT



## 7. Departmental Circular (Sample)



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### Electronics and Communication Department CIRCULAR

Date: 17.06.2022

Ref: BTIRT/EC/GN/06/2022/0228

Dear Faculty Members,

An important meeting regarding the Time Table Committee for the Odd Semester of the Academic Year 2022-2023, is plane by Principal office on Date: 20th June 2022, Time: 11:00 am, Venue: Principal's Office.

Agenda for the Meeting:

- Review of minutes from the previous semester's meetings.
- Discussion on the allocation of laboratory slots for shared laboratories for the academic year 2023-2024.
- Finalization of department electives and allied courses.
- Finalization of the Time Table for all departments.

Your presence and active participation are crucial as we discuss and finalize these important matters. Your inputs and insights will contribute significantly to the preparation and finalization of the upcoming timetable.

Please feel free to put your suggestions regarding these points, if you have any questions or need further information. Looking forward to fruitful discussions and a productive meeting.

Thank you.

Best regards,

  
Mrs. Megha Soni  
Head of Department (EC Dept)  
BTIRT Sagar





## 8. Class Time Table (Sample)



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Department of Electronics and Communication Engineering



### Time Table (III semester)

w.e.f 16 Aug 2022

DAY	SEM	Room No.	10:00-11:00AM	11:00-12:00PM	12:00-12:50PM	01:40-02:40PM	02:40-03:40PM	03:40-04:30PM
MON	III	T-28	I	II	III	IV	V & VI	V & VI
TUE	III	T-28	Mathematics-III	EMI	DSD	English	DSD Lab (A1) / EMI (A2)	
WED	III	T-28	Mathematics-III	EMI	DSD	English	DSD Lab (A2) / EMI (A1)	Sports / Library
THU	III	T-28	Mathematics-III	EMI	DSD	English	Network Analysis	GS/Aptitude
FRI	III	T-28	Network Analysis	ED Lab (A2) / EMI (A1)	Electronic Devices	GS/Aptitude	Network Analysis	Electronic Devices
SAT	III	T-28	Network Analysis	ED Lab (A1) / NA (A1)		GS/Aptitude	NA Lab (A2) / EMI (A2)	Internship-I
LUNCH 12:50-01:40PM								

Subject Name	Name of Faculty for Theory	Subject Name	Name of Faculty for Lab work	
			A1 Batch	A2 Batch
BT301 Mathematics-III	Dr. Biresh Gupta (BG)	EC 303	Mr. Vikash Panthi (VP)	Mr. Vikash Panthi
EC 302 Electronic Measurement & Design	Mr. Naveen Khare (NK)	EC 304	Mr. Ankur Namdeo (AN)	Mr. Ankur Namdeo
EC 303 Digital System	Mr. Vikash Panthi (VP)	EC 305	Miss. Diksha Jain (DJ)	Miss. Diksha Jain
EC 304 Electronic Devices	Mr. Ankur Namdeo (AN)	EC 306	Mr. Naveen Khare (NK)	Mr. Naveen Khare
EC 305 Network Analysis	Miss. Diksha Jain (DS)	EC 307	Miss. Zab Khan (ZK)	Miss. Zab Khan
VAP GS/Aptitude	Miss. Aayushi Pateriya (AP)	VAP	Mr. Mamish Shrivastav	Mr. Mamish

Mrs. Megha Soni  
HoD EC Department

Dr. Veetesh Fuskele  
Principal BTIRT, Sagar



## 9. Lesson Plan (Sample)



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Department: ELECTRONICS AND COMMUNICATION					Session: JULY- DECEMBER 2022		
Name of Faculty: Mr. Vikash Panthi					Semester: THIRD		
Subject: DIGITAL SYSTEM DESIGN					Sub Code: EC 303		
Total expected Lectures -47					Session commencement: 16 Aug		
S.NO	CONTENTS	Plan Date	Completion Date	Ref.	Teaching Methodology	CO	Remarks
Unit-1 Number Systems:							
1	Number systems, base conversions.	16-Aug	16-Aug	R3,R6	Chalk Board	EC303.1	
2	Logic gates and binary operations	17-Aug	17-Aug	R3,R6	Chalk Board	EC303.1	
3	Implementations of Logic Functions using gates	22-Aug	22-Aug	R3,R6	Chalk Board	EC303.1	
4	NAND-NOR implementations	23-Aug	23-Aug	R3,R6	Chalk Board	EC303.1	
5	Multi level gate and multi output gate implementations	24-Aug	24-Aug	R3,R6	Chalk Board	EC303.1	
6	Boolean postulates and laws	29-Aug	29-Aug	R3,R6	Chalk Board	EC303.1	
7	Sum of Products (SOP), Product of Sums (POS)	30-Aug	30-Aug	R3,R6	Chalk Board	EC303.1	
8	Boolean function, Canonical and standard forms,	5-Sep	5-Sep	R3,R6	Chalk Board	EC303.1	
9	Minimization of Boolean functions, Minterm,Maxterm,	6-Sep	6-Sep	R3,R6	Chalk Board	EC303.1	
10	Karnaugh map Minimization, Don't care conditions,	7-Sep	7-Sep	R3,R6	Chalk Board	EC303.1	
11	Quine-McCluskey method of minimization.	12-Sep	12-Sep	R3,R6	Chalk Board	EC303.1	
Unit-2 Combinational logic circuits							
12	Combinational logic circuits .Half adder – Full Adder	13-Sep	13-Sep	R3,R6	Chalk Board	EC303.1	
13	Half subtractor - Full subtractor	14-Sep	14-Sep	R3,R6	Chalk Board	EC303.2	
14	Parallel binary adder	19-Sep	19-Sep	R3,R6	Chalk Board	EC303.2	
15	parallel binary Subtractor – Fast Adder	20-Sep	20-Sep	R3,R6	Chalk Board	EC303.2	
16	Carry Look Ahead adder-	21-Sep	21-Sep	R3,R6	Chalk Board	EC303.2	
17	Serial. Adder/Subtractor	26-Sep	26-Sep	R3,R6	Chalk Board/PPT	EC303.2	
18	BCD adder – Binary Multiplier	27-Sep	27-Sep	R3,R6	Chalk Board/PPT	EC303.2	
19	Binary Divider -Multiplexer/De-multiplexer	28-Sep	28-Sep	R3,R6	Chalk Board/PPT	EC303.2	
20	Decoder - encoder – parity checker – parity generators	10-Oct	10-Oct	R3,R6	Chalk Board/PPT	EC303.2	
21	Codeconverters - Magnitude Comparator.	11-Oct	11-Oct	R3,R6	Chalk Board/PPT	EC303.2	
Unit-3. Sequential Logic Design							
22	Sequential Logic Design: Building blocks like S-R, JK	12-Oct	12-Oct	R3,R6	Chalk Board/PPT	EC303.2	
23	Master-Slave JK FF,	17-Oct	17-Oct	R3,R6	Chalk Board/PPT	EC303.3	
24	Edge triggered FF,	18-Oct	18-Oct	R3,R6	Chalk Board/PPT	EC303.3	
25	Finite state machines,	19-Oct	19-Oct	R3,R6	Chalk Board/PPT	EC303.3	
26	Design of synchronous FSM	31-Oct	31-Oct	R3,R6	Chalk Board/PPT	EC303.3	
27	Algorithmic State Machines charts.	7-Nov	7-Nov	R3,R6	Chalk Board/PPT	EC303.3	
28	Designing synchronous circuits like Pulse train generator	9-Nov	9-Nov	R3,R6	Chalk Board/PPT	EC303.3	
29	Pseudo Random Binary Sequence generator, Clock generation	14-Nov	14-Nov	R3,R6	Chalk Board/PPT	EC303.3	



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UNIT 4 Registers and Counters							
30	Registers and Counters: Asynchronous Ripple or serial counter.	15-Nov	15-Nov	R3,R6	Chalk Board/PPT	EC303.3	
31	Asynchronous Up/Down counter -	16-Nov	16-Nov	R3,R6	Chalk Board/PPT	EC303.3	
32	Synchronous counters, Synchronous Up/Downcounters	21-Nov	21-Nov	R3,R6	Chalk Board/PPT	EC303.3	
33	Programmable counters	22-Nov	22-Nov	R3,R6	Chalk Board/PPT	EC303.3	
34	Design of Synchronous counters:	23-Nov	23-Nov	R3,R6	Chalk Board/PPT	EC303.3	
35	State diagram-State table –State minimization	28-Nov	28-Nov	R3,R6	Chalk Board/PPT	EC303.4	
36	State assignment - Excitation table and maps-Circuit	29-Nov	29-Nov	R3,R6	Chalk Board/PPT	EC303.4	
37	Implementation of Modulo-n counter,	30-Nov	30-Nov	R3,R6	Chalk Board/PPT	EC303.4	
38	Registers – shift registers - Universal shift registers	5-Dec	5-Dec	R3,R6	Chalk Board/PPT	EC303.4	
Unit-5 Logic Families and Semiconductor Memories:							
39	Logic Families and Semiconductor Memories:	6-Dec	6-Dec	R3,R6	Chalk Board/PPT	EC303.4	
40	TTL NAND gate	7-Dec	7-Dec	R3,R6	Chalk Board/PPT	EC303.4	
41	Specifications, Noise margin, Propagation delay,	12-Dec	12-Dec	R3,R6	Chalk Board/PPT	EC303.4	
42	fan-in, fan-out,	13-Dec	13-Dec	R3,R6	Chalk Board/PPT	EC303.4	
43	Tristate TTL,	14-Dec	14-Dec	R3,R6	Chalk Board/PPT	EC303.4	
44	ECL, CMOS families and their interfacing,	19-Dec	19-Dec	R3,R6	Chalk Board/PPT	EC303.4	
45	Memory elements,	20-Dec	20-Dec	R3,R6	Chalk Board/PPT	EC303.5	
46	Concept of Programmable logic devices like FPGA.	21-Dec	21-Dec	R3,R6	Chalk Board/PPT	EC303.5	
47	Logic implementation using Programmable Devices.	26-Dec	26-Dec	R3,R6	Chalk Board/PPT	EC303.5	



*Megha*

## 10. Academic Monitoring



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### Department of Electronics and Communication Engineering

Academic Year 2022 - 2023

#### CIRCULAR


Date: 10.10.2022

The meeting for departmental academic monitoring for current semester Electronics and communication Engineering is to be held in the HOD Cabin on 11/10/2022 at 10.40 AM. Students are expected to inform about their difficulties and requirements. All the faculty members are invited to attend the meeting.

#### Agenda:

1. Review of odd Semester Syllabus.
2. Academic Rules and Regulations.
3. Regarding student's attendance and Internal Exams.
4. University lab and theory sessional exam preparation.



  
Mrs. Megha Soni  
HOD ECE

## 11. Internal Test Circular & Schedule



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0608

BTIRT/EXAM/SEPT-2022/001

Date: - 26.09.2022

### DEPARTMENT OF EXAMINATION

#### NOTICE

All the faculty members are hereby informed that mid semester examination will be held for B. Tech 2<sup>nd</sup> and 3<sup>rd</sup> year from 29<sup>th</sup>, 30<sup>th</sup> September and 1<sup>st</sup> October 2022.

All of you are requested not to avail any leave during above mentioned dates. If necessary, get your leave sanctioned from the Principal in advance & report the same to the exam department also.

Kindly submit 3<sup>rd</sup>& 5<sup>th</sup> semester question paper by today evening at 5:30pm.

  
Exam Superintendent



  
Principal

## 12. Internal Test Time Table (Sample)



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**B.TECH MID-SEMESTER EXAMINATION (AUGUST-2022)**  
**TIME TABLE**

Course	30/08/2022		31/08/2022	
	Tuesday		Wednesday	
	10:30 am - 12:30 pm	3:00 pm - 5:30 pm	10:30 am - 12:30 pm	3:00 pm - 4:30 pm
B.TECH. VII	VII SEM (CE)	CE-701 Geotechnical Engg	CE-702 (D) Structural Design and Drawing (RCC-II)	CE-706 Major Project-I (Presentation Phase I)
	VII SEM (CS)	CS-703(A) Cryptography & Information Security	CS 701 Software Architecture	CS-706 Major Project-I (Presentation Phase I)
	VII SEM (EC)	EC-702(A) Microwave Engg	EC-703(B) Internet of Things	EC-706 Major Project-I (Presentation Phase I)
	VII SEM (EX)	EX-701 Power System Protection	EX-702(C) High Voltage Engineering	EX-706 Major Project-I (Presentation Phase I)
	VII SEM (ME)	ME-701 Heat and Mass Transfer	ME-702 (C) Power Plant Engineering	ME-706 Major Project-I (Presentation Phase I)

  
EXAMINATION DEPARTMENT



## 13.RGPV University Question Papers



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## 10.RGPV University Question Papers

Total No. of Questions : 8]

[Total No. of Printed Pages : 4

Roll No .....

### EC-303-CBGS

### B.Tech., III Semester

Examination, December 2020

## Choice Based Grading System (CBGS)

### Digital System Design

Time : Three Hours

Maximum Marks : 70

- Note:** i) Attempt any five questions out of eight.  
आठ प्रश्नों में से किन्हीं पाँच प्रश्नों को हल कीजिए।
- ii) All questions carry equal marks.  
सभी प्रश्नों के समान अंक हैं।
- iii) Assume suitable data, if required.  
यदि आवश्यक हो तो उपयुक्त डाटा मानें।
- iv) In case of any doubt or dispute the English version question should be treated as final.  
किसी भी प्रकार के संदेह अथवा विवाद की स्थिति में अंग्रेजी भाषा के प्रश्न को अंतिम माना जायेगा।
1. a) Explain the difference between positive and negative logic giving an example of each as applied to voltage levels.  
सकारात्मक और नकारात्मक तर्क के बीच अंतर को एक उदाहरण के साथ समझाइये जो प्रत्येक वोल्टेज स्तर पर लागू होते हैं।
- b) Differentiate between analog and digital circuits.  
एनालॉग और डिजिटल सर्किट के बीच अंतर करें।



EC-303-CBGS

PTO



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[2]

2. a) i) Find the binary equivalent of 4557.  
4557 के बाइनरी समकक्ष खोजें।  
ii) Find the decimal equivalent of 100110111.  
100110111 के दशमलव समतुल्य को खोजें।  
b) Convert the following:  
निम्नलिखित कन्वर्ट करें।  
i)  $(10011100010)_2 = ( )_{10}$   
ii)  $1111.1011 = ( )_2$   
iii)  $(67)_{10} = ( )_2$   
iv)  $(68.87)_{10} = ( )_2$   
v)  $(444)_{10} = ( )_2$
3. a) With the help of suitable example explain BCD, Excess 3 and Gray code.  
उपयुक्त उदाहरण की मदद से BCD, Excess 3 और Gray code की व्याख्या करें।  
b) State and prove De-Morgan's theorem.  
डी-मॉर्गन के प्रमेय को लिखिए और साबित करें।
4. a) Explain the designing of synchronous FSM.  
सिंक्रोनस एफ एस एम की डिजाइनिंग के बारे में बताइए।  
b) Minimize the following using Karnaugh Map:  
 $F(A, B, C, D) = \sum(0,5,7,9,11,13)$   
कर्णघ मानचित्र का उपयोग करके निम्नलिखित को कम करें :

EC-303-CBGS

Contd...







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[3]

$$F(A, B, C, D) = \sum(0,5,7,9,11,13)$$

5. a) Explain the following terms:
- Fan-in
  - Fan-out
  - Propagation delay
- निम्नलिखित के बारे में बताएँ
- फैन-इन
  - फैन-आउट
  - प्रचार देरी (प्रोपेगेशन डिले)
- b) Draw the diagram of Tri-state TTL and explain its working.  
ट्राई-स्टेट टि-टि-एल को चित्र द्वारा विस्तार में बताएँ।
6. a) Convert the following.
- $(1.375)_{10}$  to Binary
  - Gray code 1101 to Binary code
- कन्वर्ट करें -
- $(1.375)_{10}$  को बायनरी में
  - ग्रे कोड 1101 को बायनरी कोड में
- b) What is universal gate? Why multiplexer is called universal gate?  
युनिवर्सल गेट क्या है? मल्टीप्लेक्सर को युनिवर्सल गेट क्यों कहते हैं?

EC-303-CBGS

PTO





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[4]

7. a) Draw the circuit of basic ECL inverter and explain its operations.  
बुनियादी ECL इन्वर्टर के सर्किट को बनाइये और इसके संचालन की व्याख्या करें।
- b) Discuss Resistor Transistor Logic (RTL)? Explain how it performs the NOR logic function?  
प्रतिरोधी ट्रान्जिस्टर लॉजिक (RTL) पर चर्चा करें। यह NOR लॉजिक पर कैसे कार्य करता है, समझाइये।
8. Write short note (Any three)  
संक्षिप्त नोट लिखिए। (कोई चार)
- a) POS
- b) Quine McCluskey Method
- c) Parity Checker
- d) Pulse Train generator

\*\*\*\*\*



Roll No .....

**EC-303 (CBGS)****B.Tech., III Semester**

Examination, May 2019

**Choice Based Grading System (CBGS)****Digital System Design**

Time : Three Hours

Maximum Marks : 70

**Note:** i) Attempt any five questions out of eight.

आठ प्रश्नों में से किन्हीं पाँच प्रश्नों को हल कीजिए।

ii) All questions carry equal marks.

सभी प्रश्नों के समान अंक हैं।

iii) Assume suitable data, if required.

यदि आवश्यक हो तो उपयुक्त डाटा मानें।

iv) In case of any doubt or dispute the English version question should be treated as final.

किसी भी प्रकार के संदेह अथवा विवाद की स्थिति में अंग्रेजी भाषा के प्रश्न को अंतिम माना जायेगा।

1. a) Explain the process of conversion from Octal to Hexadecimal System.

ऑक्टल से हेक्साडेसिमल सिस्टम में रूपांतरण की प्रक्रिया को समझाइए।

b) Explain some of the differences between analog and digital circuits.

एनालॉग और डिजिटल सर्किट के बीच कुछ अंतर बताइए।

2. a) i) Find the binary equivalent of 4557.

4557 के बाइनरी समकक्ष खोजें।

ii) Find the decimal equivalent of 100110111.

100110111 के दशमलव समतुल्य को खोजें।

b) Convert the following:

निम्नलिखित कन्वर्ट करें।

i)  $(10011100010)_2 = ( )_{10}$ ii)  $1111.1011 = ( )_2$ iii)  $(67)_{10} = ( )_2$ iv)  $(68.87)_{10} = ( )_2$ v)  $(444)_{10} = ( )_2$ 

3. a) With the help of suitable example explain ASCII Code.

उपयुक्त उदाहरण की मदद से ASCII कोड की व्याख्या करें।

b) Write and explain the principle of Duality.

द्वंद्व के सिद्धांत को लिखें और समझाइए।

4. a) Explain the designing of synchronous FSM.

सिंक्रोस एफ एस एम की डिजाइनिंग के बारे में बताइए।

b) Minimize the following using Karnaugh Map:

 $F(A, B, C, D) = \Sigma(0,5,7,9,11,13)$ 

कर्णघ मानचित्र का उपयोग करके निम्नलिखित को कम करें :

 $F(A, B, C, D) = \Sigma(0,5,7,9,11,13)$ 

## 14. Continuous Internal Assessment



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### Continuous Internal Assessment (CIA)

*"When an assessment is made, the judgment is not of whether the performance of a person was 'good' or 'bad'. Rather it is, of the performance of a task, whether it was 'good' or 'poor'".*

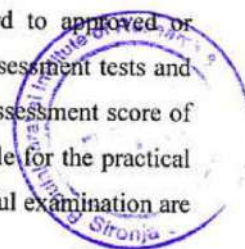
**Woods et al. (1988)**

At BTIRT College CIA is the regular assessment during the semester which contributes to the final results of students. It provides regular feedback to students. Exercises, assignments, and progress tasks given to students throughout an academic semester work as tools of continuous assessment.

In internal assessment, the learner and the teacher are directly involved in assessing the student's learning progress. CIA is commissioned by the RGPV University and carries 50 marks. The primary purpose of this type of assessment should be to provide students with regular feedback to stimulate learning and to provide students with information to help them evaluate the effectiveness of their learning strategies. Exercises, assignments, and progression tasks given to students throughout the academic semester or year can serve as formative assessment tools. Through this type of assessment, students practice essential skills such as writing, communication, ethics, arithmetic, problem solving, drawing, using scientific equipment, or operating equipment without fear of failure.

At BTIRT College, as per the provisions of RGPV Bhopal Examination Regulations, there will be four components for CIA in each semester: Attendance, Mid-Semester Examination, Quiz/Assignment(s) and Term Paper (including Laboratory Work and Sessions, Practical, Sub-Project I and Sub-Project II, Main Project I and Main Project II, Field Visit, Case Study and Seminar). The continuous evaluation of the semester papers takes place weekly. At least two intermediate examinations are carried out in a semester. There will be at least one quiz/assignment per unit per semester.

- A. Attendance:** Attendance will be part of the internal assessment. Grades or attendance are tabulated based on a student's actual attendance, without regard to approved or unauthorized leaves of absence. The average grades of the internal assessment tests and homework, together with the attendance rate, form the final internal assessment score of the candidate in the theoretical part of that particular coursework, while for the practical part of a coursework, the grades obtained by the students after successful examination are





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considered the final internal assessment score. The five attendance grades are tabulated based on a student's actual attendance, without regard to authorized or unauthorized leaves of absence, using the following parameters:

100-90% = 5 Marks

89-85% = 4 Marks

84-80% = 3 Marks

79-75% = 2 Marks

74-70% = 1 Mark

69% and below=No Marks.

- B. Mid Semester Examination:** Two internal assessment mid semester examination, of 1.5hr and 3hr duration respectively will be conducted periodically in each semester.
- C. Quiz/ Assignment:** One assignment such as a quiz, term paper, book review, group discussion, or power-point presentation on a topic at the discretion of the course teacher. One assignment per paper should be submitted on the topic assigned by the respective teachers. The average of the assignment(s) will account for 10 marks.
- D. Term work - Term work (including Lab Work & Sessional, Internship, Minor Project-I and Minor Project-II, Major Project-I and Major Project-II, Field Visit, Case Study and Seminar ).**
- Lab work & Sessional:** Two session tests will be held for each subject/paper in the middle of a semester. Each session test will be awarded 20/10 marks (as per RGPV New Scheme). Participation in the sessional tests is mandatory.
  - Minor Project-I and Minor Project-II-**
  - Major Project-I and Major Project-II -**
  - Internship -**

### Note:

- It is mandatory for the student to appear for the tests and submit the assignment.
- The structure of these Internal Assessment Tests and End Semester Examinations shall be in a 33.33%: 66.66% ratio.
- A minimum of 2 hours per week should be allotted for the Virtual Lab along with the slot fixed for the conventional lab classes.
- MST: Minimum of two mid semester tests to be conducted.
- Students can earn additional credits from the University recognized MOOC courses.

For Continuous Internal Assessment, the following principles should be observed:





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1. The students should be informed of the grade/marks as quickly as possible. The purpose of this feedback is to provide the students with information on the level of their performance in relation to the teacher' expectations.
2. The teacher should provide detailed and constructive criticism so that students know what is expected of them and how they might improve their performance.
3. Provides remedial classes for slow learner. Provide more resources, and attention to fulfill teacher' expectations.
4. Encourage and appreciate fast learner.

Continuous internal assessment means that the teacher is continually aware of students' overall development, knowledge, strengths and weaknesses. It is a process that takes place over a period of time and contributes to the gradual construction of a cumulative assessment of student performance. It also allows a teacher to look for signs that indicate desired goals are being achieved, such as: B. the different skills that the lessons target. Also helps the teacher to plan his remedial materials/exercises based on diagnosing the students' difficulties, problems, etc. At BTIRT, we discourage harmful comments, hollow comments, misleading comments, zero comments and negative comments during internal reviews.





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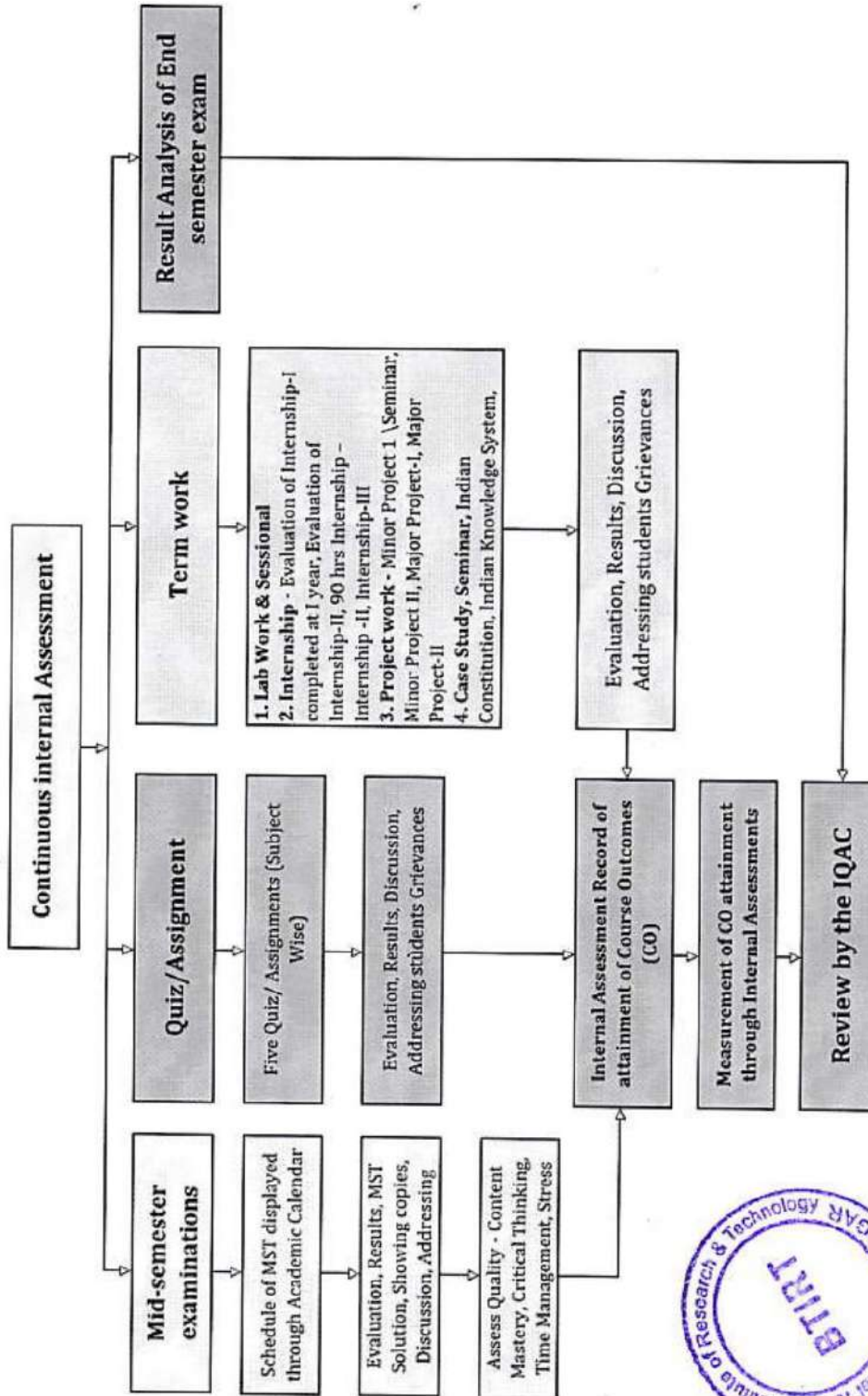


Figure 1: Continuous Internal Assessment (Annexure 1.1.1 M)

# Lab work & Sessional - Continuous Internal Assessment (CIA) Record

Digital System Design

July - Dec 2022

SRL	ENRL	NAME	EC-303T										EC-303 T										
			MST		U-1		U-2		U-3		U-4		U-5		Lab Work	File/Viva	ATT	File	ATT	ES	MST	OA	LWS
			50% of MST	20	20	20	20	20	20	20	20	20	20	20									
1	0608EC211001	AAFREEN KHAN	0	3	2	12	13	14	12	13	7	3	2	3	2	3	4	5	10	20	10	20	20
2	0608EC211002	AAKASH KURMI	0	0	3	2	13	12	12	12	7	3	2	2	3	4	5	6	12	5	6	12	12
3	0608EC211003	AAKASH VISHWAKARMA	11	6	3	4	18	17	18	18	7	5	4	4	8	13	9	16	13	13	9	16	16
4	0608EC211006	ABHISHEK THAKUR	0	0	3	2	13	12	12	13	8	3	3	3	3	4	5	6	14	5	6	14	14
5	0608EC211007	AKHLESH KURMI	5	3	3	2	18	19	18	19	6	3	2	3	4	8	9	11	8	8	9	11	11
6	0608EC211009	ANIKET NAMDEV	0	0	3	2	12	12	12	13	7	3	2	3	4	5	6	12	4	5	6	12	12
7	0608EC211010	ANIKET SAHU	8	4	3	4	12	12	13	12	9	4	4	4	8	11	6	17	4	5	6	12	12
8	0608EC211012	ANUJ SINGH KURMI	0	0	3	2	12	13	12	11	12	7	3	2	3	4	5	6	12	5	6	12	12
9	0608EC211016	ARTI KOSHTI	5	3	5	5	18	17	18	19	8	4	5	5	9	14	7	17	5	10	9	17	17
10	0608EC211017	CHAHINA KUSHWAHA	9	5	5	5	15	14	15	16	14	8	4	5	4	5	10	9	10	9	17	17	
11	0608EC211019	DEEPAK KUMAR LODHI	9	5	3	3	18	18	18	18	8	5	4	4	5	8	6	13	5	8	6	13	13
12	0608EC211020	DEEPAK PATKAR	4	2	3	3	12	13	12	12	8	4	4	4	3	4	5	6	16	4	5	6	16
13	0608EC211022	DEEPAK VISHWAKARMA	0	0	3	2	13	12	13	12	8	4	4	4	3	4	5	6	16	4	5	6	16
14	0608EC211023	DEVI SINGH RAJPOOT	0	0	4	3	18	19	18	19	9	4	4	4	3	4	5	6	16	4	5	6	16
15	0608EC211024	DHRUV NANDAN UPADHYAY	4	2	4	3	13	14	14	15	14	7	3	3	3	3	4	5	8	7	9	17	17
16	0608EC211025	DURGA PATEL	10	5	5	5	18	18	18	18	9	4	5	5	9	15	9	18	5	15	9	18	18
17	0608EC211026	GOURAV DOHARE	0	0	3	2	13	12	13	12	8	4	4	4	3	4	5	6	16	4	5	6	16
18	0608EC211028	HARSHIT KHARE	20	10	4	2	14	15	14	15	14	7	3	3	3	4	5	6	16	4	5	6	16
19	0608EC211029	HIMANSHU CHOUDHARY	0	0	3	2	12	13	14	12	8	3	3	2	3	4	5	6	16	4	5	6	16
20	0608EC211032	KULDEEP DANGI	11	6	3	2	13	14	13	14	6	3	2	3	4	5	6	16	4	5	6	16	
21	0608EC211033	MANGAL SINGH LODHI	19	10	4	4	14	15	16	17	18	9	3	4	3	4	5	6	16	4	5	6	16
22	0608EC211035	MAYANK SAHU	17	9	3	3	16	15	16	15	7	3	2	3	4	5	6	16	4	5	6	16	
23	0608EC211037	NIKHIL CHOURASIYA	2	1	3	2	13	14	15	14	14	7	3	3	3	4	5	6	16	4	5	6	16
24	0608EC211038	NISHA PRAJAPATI	8	4	5	5	15	16	17	18	19	7	3	5	4	9	14	9	15	7	9	13	13
25	0608EC211040	PIYUSH CHOURASIYA	4	2	3	2	18	19	18	19	18	7	4	2	4	4	5	6	16	4	5	6	16
26	0608EC211041	POONAM RAJ SURYAVANSHI	18	9	5	5	19	18	18	18	18	9	5	5	5	10	19	9	19	9	19	19	19
27	0608EC211044	PRIYANK GUPTA	1	1	4	4	14	15	14	15	14	9	4	4	4	8	9	7	15	4	4	4	4
28	0608EC211045	PRIYANKA VISHWAKARMA	1	1	4	4	15	14	15	15	14	9	4	4	4	8	9	7	15	4	4	4	4
29	0608EC211048	RIYA THAKUR	18	9	5	5	14	15	16	16	15	9	5	5	5	9	19	9	19	9	19	19	19
30	0608EC211049	SAKSHI KURMI	18	9	4	3	13	15	16	15	16	9	5	3	3	4	5	6	16	4	5	6	16
31	0608EC211051	SHASHANK GUPTA	2	1	3	2	14	13	14	14	15	7	3	2	3	4	5	6	16	4	5	6	16
32	0608EC211053	SHERIK ANT RAJPUT	12	6	5	5	18	19	18	19	18	7	4	5	4	9	16	16	16	7	7	12	12
33	0608EC211057	TRIVENDRA SEN	15	8	5	5	15	16	14	14	15	8	4	4	4	9	17	17	17	8	8	13	13
34	0608EC211059	VARSHA SONI	7	4	2	2	13	12	13	12	13	8	3	3	3	4	5	6	16	4	5	6	16
35	0608EC211060	VISHESH VISHWAKARMA	15	8	5	5	17	18	17	18	17	9	5	5	5	9	19	19	19	8	8	13	13





# 15. Course Outcomes (CO) and Measurement of CO attainment through Continuous Internal Assessments

Digital System Design

July – Dec 2022

## 6) Course Outcomes (COs)

After the completion of the course the student will be able to –

Course Name: EC- 303 Digital System Design	
EC303.1	Proficiency in understanding number systems, codes, and logic gates, with the ability to implement logic functions using gates.
EC303.2	Competence in designing and analyzing combinational logic circuits for various applications.
EC303.3	Skills in designing sequential logic circuits, including finite state machines and synchronous circuits.
EC303.4	Proficiency in designing and analyzing registers and counters for various counting and shifting operations.
EC303.5	Comprehensive understanding of logic families, semiconductor memories, and programmable logic devices for logic implementation.

## 7) Course Outcomes vs. Programme Specific Outcomes Mapping

Course Name: EC- 303 Digital System Design			
COs	PSO1	PSO2	PSO3
EC303.1	3	2	2
EC303.2	3	2	2
EC303.3	3	3	3
EC303.4	3	3	2
EC303.5	3	2	3
Average	3	2.4	2.4
Course	PSO1	PSO2	PSO3
EC - 303	3	2.4	2.4

## 8) Course Outcomes vs. Programme Outcomes Mapping

Course Name: EC- 303 Digital System Design												
COs	P01	P02	P03	P04	P05	P06	P07	P08	P09	P010	P011	P012
EC303.1	2	2	-	2	-	-	-	-	-	-	-	1
EC303.2	1	1	-	1	-	-	-	-	-	-	-	1
EC303.3	1	2	-	-	-	-	-	-	-	-	-	-
EC303.4	2	2	1	2	1	-	-	-	-	-	-	1
EC303.5	2	1	1	1	-	-	-	-	-	-	-	-
Course	P01	P02	P03	P04	P05	P06	P07	P08	P09	P010	P011	P012
EC- 303	1.6	1.6	0.4	1.2	0.2	0	0	0	0	0	0	0.6



**Measurement of CO attainment through Continuous Internal Assessments**

Subject:		Subject Code: EC-303 Semester: III									
Digital System Design		MSE I			MSE II			Quiz/Assignment			
		CO1	CO2	CO3	CO4	CO5	CO1	CO2	CO3	CO4	CO5
No. of Students Scoring more than or equal to 50%		17	17	17	17	17	36	36	36	36	36
% of Students Scoring more than or equal to 50%		48%	48%	48%	48%	48%	100%	100%	100%	100%	100%
Attainment level 1 for $\geq 35\%$ 2 for $\geq 50\%$ 3 for $\geq 75\%$		1.9	1.9	1.9	1.9	1.9	3	3	3	3	3



28) Dec 2022 - University Results

S.N.	Roll No.	Name of Candidate	THEORY SECTION										PRACTICAL SECTION				RESULT	
			BT-301	EC-302	EC-303	EC-304	EC-305	BT-107	EC-304	EC-303	EC-304	EC-305	EC-306	SGPA	CGPA	PASS	FAIL	
1	0608EC211002	AAKASH KURMI	C	F	D	C	F	C	F	B+	B+	B+	B	A	4.25	4.05	PASS	FAIL
2	0608EC211003	AAKASH VISHWAKARMA	C+	C+	C+	C+	C+	C+	C+	C+	C+	C+	C+	C+	6.92	6.53	PASS	PASS
3	0608EC211006	ABHISHEK THAKUR	F	C	F	C	F	C	F	B+	B+	B+	B+	B+	3.71	3.79	FAIL	FAIL
4	0608EC211007	AKHILESH KURMI	B	C	B	C	F	C	F	B+	B+	B+	B+	B+	6.54	5.51	PASS	PASS
5	0608EC211025	ANIKET NAMDEV	B	C+	C+	B	C	F	C	A+	A+	A+	A+	A+	6.71	6.46	FAIL	FAIL
6	0608EC211010	ANIKET SAHU	C	C+	C+	B	C	F	C	A+	A+	A+	A+	A+	6.83	5.61	PASS	PASS
7	0608EC211012	ANJLI SINGH KURMI	C	F	F	F	F	F	F	A	B+	B+	B+	B+	3.25	4.62	FAIL	FAIL
8	0608EC211016	ARTI KOSHITI	B	C+	C+	C+	C+	C+	C+	A+	A+	A+	A+	A+	7.53	7.51	PASS	PASS
9	0608EC211017	CHAIRNA KUSHWAHA	B	C+	C+	B	C	F	C	A+	A+	A+	A+	A+	7.54	7.15	PASS	PASS
10	0608EC211019	DEEPAK KUMAR LODHI	B	C+	C+	B	C	F	C	A	B+	B+	B+	B+	7.13	6.67	PASS	PASS
11	0608EC211020	DEEPAK PATIL	C+	C	D	C	D	C	F	A	B+	B+	B+	B+	5.38	5.81	FAIL	FAIL
12	0608EC211022	DEEPIK VISHWAKARMA	C+	C+	C	C+	C	DH	DH	B	B+	B	B	B	6.25	4.98	PASS	PASS
13	0608EC211024	DEVI SINGH RAJPOOT	B	C	C	C+	C	C	C	A	A	A	A	A	6.13	5.83	FAIL	FAIL
14	0608EC211025	DURGAPATEL	B	C+	C	C	C	C	C	A	A+	A+	A+	A+	6.42	5.87	PASS	PASS
15	0608EC211026	GOURAV DOHARE	C	C	F	F	F	F	F	B+	B+	B+	B+	B+	3.92	3.85	FAIL	FAIL
16	0608EC211028	HARSHIT KHARE	C+	C+	C	C	C	C	C	A+	A+	A+	A+	A+	6.92	6	PASS	PASS
17	0608EC211029	HIMANSHU CHOUHDHARY	B	F	F	F	F	F	F	B	B	B	B	B	3.38	3.62	FAIL	FAIL
18	0608EC211032	KULDEEP DANGE	C+	C	C	C	C	C	C	A	A	A	A	A	5.88	5.05	PASS	PASS
19	0608EC211033	MANGAL SINGH LODHI	B	C	B+	C	B+	C	DH	A	B+	B	B	B	7.42	6.73	PASS	PASS
20	0608EC211035	MAYANK SAHU	B+	C+	B	B	B	B	C	A	A	A	A	A	7.42	6.65	PASS	PASS
21	0608EC211037	NIRHIL CHOURASIVA	B+	C+	B	B	B	B	C	A	A	A	A	A	5.71	7.45	PASS	PASS
22	0608EC211038	NISHA PRAJAPATI	B+	B	B	B	B	B	C	A+	A+	A+	A+	A+	7.75	7.45	PASS	PASS
23	0608EC211040	PIYUSH CHOURASIVA	C+	C	C+	C	C	C	DH	A	B+	B+	B+	B+	6.04	5.07	PASS	PASS
24	0608EC211044	PRIVANK GUPTA	B+	C	C	B	B	B	C	A+	A+	A+	A+	A+	7.92	8.26	PASS	FAIL
25	0608EC211045	PRIVANK VISHWAKARMA	C	C	C	C	C	C	C	A	A	A	A	A	5.08	6.08	FAIL	FAIL
26	0608EC211048	PRIVANK VISHWAKARMA	C	C+	C	C	C	C	C	B+	B+	B+	B+	B+	6.38	6.8	PASS	PASS
27	0608EC211049	RIYA THAKUR	B	C+	B	B	B	B	C+	A	A	A	A	A	7.38	6.92	PASS	PASS
28	0608EC211049	SAKSHI	B	C+	B	B	B	B	C	A	A	A	A	A	7.79	7.24	PASS	FAIL
29	0608EC211051	SHASHANK GUPTA	C+	C+	C	C	C	C	F	A	B+	B+	B+	B+	5.79	5.3	FAIL	FAIL
30	0608EC211051	SHIKHANT RAJPUT	B+	B	B+	B	B+	B	C+	A+	A+	A+	A+	A+	8.25	8.01	PASS	PASS
31	0608EC211053	SHIKHANT RAJPUT	B+	B	B+	B	B+	B	C+	A+	A+	A+	A+	A+	8	7.48	PASS	PASS
32	0608EC211057	TRIVENDRA SEN	B+	B	B	B	B	B	C	A	B+	B+	B+	B+	5.29	4.98	FAIL	FAIL
33	0608EC211059	VAISHA SONI	C	C	C	C	C	C	F	A	B+	B	B	B	5.29	6.06	FAIL	FAIL
34	0608EC211060	VISHESH VISHWAKARMA	C+	F	C	C	C	C	F	A+	A+	A+	A+	A+	5.25	4.45	FAIL	FAIL
35	0608EC221001	AJAY AHIRWAR	B	F	C	C	C	C	F	A	B+	B+	B+	B+	4.45	4.45	FAIL	FAIL
36	0608EC221002	CHANDRAKANT PRAJAPATI	C+	C+	C	C	C	C	C	A	B+	B+	B+	B+	6.36	6.36	PASS	PASS
37	0608EC211006	DEEPIK SINGH DANGE	C	F	F	F	F	F	F	A	B+	B+	B+	B+	3.88	3.86	FAIL	FAIL

Total Students	37	%	
Pass	22	59.5	
Fail	15	40.5	

TOPPER LIST		
Rank	Enrollment No	Name
I	0608EC211055	SHRIKANT RAJPUT
II	0608EC211057	TRIVENDRA SEN
III	0608EC211041	MOONAM RAJ SURYA VANSHI
		SGPA
		8.01
		7.48
		7.92
		8.26



